

File : US1499PA-CIP

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Phase-Interpolation Circuit

Cross Reference To Related Applications

This is a continuation-in-part of Application No. 10/079,866 filed on Feb 21, 2002, entitled "A Phase-Interpolation Circuit and A Phase-Interpolation Signal
5 Generating Device Applying the Same" and assigned to the same assignee, the contents of which are incorporated herein by reference.

Background of the invention

(1) Field of the invention

[0001] The present invention relates to a phase-interpolation circuit, more
10 particularly, a phase-interpolation circuit which can avoid short-circuit current and generate linearly distributed phase-interpolation signals.

(2) Description of the Prior Arts

[0002] Multiphase systems are widely applied in the data-recovery systems
and the phase-lock loop circuits. The multiphase systems are also a main trend
15 of design in the communication systems nowadays.

[0003] In a conventional data recovery system, after applying the equalizer to
recover high frequency decayed signals resulting from noise of communication